

D	A	PAD	ALT0	ALT1	ALT2	ALT3	ALT4	ALT5	ALT6	ALT7	ALT8	ALT9	ADC	ACMP
0		GPIO_AD_B0_03	FLEXCAN2_RX	XBAR_INOUT17	LPUART6_RXD	USB_OTG1_OC	FLEXPWM1_PWM1_X	GPIO1_I003	REF_CLK_24M	LPSP13_PCS0				ACMP4_IN4
1		GPIO_AD_B0_02	FLEXCAN2_TX	XBAR_INOUT16	LPUART6_TXD	USB_OTG1_PWR	FLEXPWM1_PWM0_X	GPIO1_I002	LPI2C1_HREQ	LPSP13_SIN				ACMP3_IN4
2		GPIO_EMC_04	SEMC_DATA04	FLEXPWM4_PWM2_A	SAI2_TX_DATA	XBAR_INOUT06	FLEXI01_D04	GPIO4_I04	USB_PHY1_TSTO_RX_DISCON_DET					
3		GPIO_EMC_05	SEMC_DATA05	FLEXPWM4_PWM2_B	SAI2_TX_SYNC	XBAR_INOUT07	FLEXI01_D05	GPIO4_I05	USB_PHY1_TSTO_RX_HS_RXD					
4		GPIO_EMC_06	SEMC_DATA06	FLEXPWM2_PWM0_A	SAI2_TX_BCLK	XBAR_INOUT08	FLEXI01_D06	GPIO4_I06	USB_PHY2_TSTO_RX_FS_RXD					
5		GPIO_EMC_08	SEMC_DM0	FLEXPWM2_PWM1_A	SAI2_RX_DATA	XBAR_INOUT17	FLEXI01_D08	GPIO4_I08	USB_PHY1_TST1_TX_DP					
6		GPIO_B0_10	LCD_DATA06	TMR4_TIMER1	FLEXPWM2_PWM2_A	SAI1_TX_DATA3, SAI1_RX_DATA1	FLEXI02_D10	GPIO2_I010	SRC_BT_CFG06		ENET2_CRS			
7		GPIO_B1_01	LCD_DATA13	XBAR_INOUT15	LPUART4_RXD	SAI1_TX_DATA0	FLEXI02_D17	GPIO2_I017	FLEXPWM1_PWM3_B		ENET2_RDATA0	FLEXI03_D17		
8		GPIO_B1_00	LCD_DATA12	XBAR_INOUT14	LPUART4_TXD	SAI1_RX_DATA0	FLEXI02_D16	GPIO2_I016	FLEXPWM1_PWM3_A		ENET2_RX_ER	FLEXI03_D16		
9		GPIO_B0_11	LCD_DATA07	TMR4_TIMER2	FLEXPWM2_PWM2_B	SAI1_TX_DATA2, SAI1_RX_DATA2	FLEXI02_D11	GPIO2_I011	SRC_BT_CFG07		ENET2_COL			
10		GPIO_B0_00	LCD_CLK	TMR1_TIMER0	MQS_RIGHT	LPSP14_PCS0	FLEXI02_D00	GPIO2_I000	SEMC_CSX1		ENET2_MDC			
11		GPIO_B0_02	LCD_HSYNC	TMR1_TIMER2	FLEXCAN1_TX	LPSP14_SOUT	FLEXI02_D02	GPIO2_I002	SEMC_CSX3		ENET2_1588_EVENT0_OUT			
12		GPIO_B0_01	LCD_ENABLE	TMR1_TIMER1	MQS_LEFT	LPSP14_SIN	FLEXI02_D01	GPIO2_I001	SEMC_CSX2		ENET2_MDIO			
13		GPIO_B0_03	LCD_VSYNC	TMR2_TIMER0	FLEXCAN1_RX	LPSP14_SCK	FLEXI02_D03	GPIO2_I003	WDOG2_RESET_B_DEB		ENET2_1588_EVENT0_IN			
14	A0	GPIO_AD_B1_02	USB_OTG1_ID	TMR3_TIMER2	LPUART2_TXD	SPDIF_OUT	ENET_1588_EVENT2_OUT	GPIO1_I018	USDHC1_CD_B	KPP_ROW6		FLEXI03_D02	ADC1_IN7, ADC2_IN7	ACMP1_IN3
15	A1	GPIO_AD_B1_03	USB_OTG1_OC	TMR3_TIMER3	LPUART2_RXD	SPDIF_IN	ENET_1588_EVENT2_IN	GPIO1_I019	USDHC2_CD_B	KPP_COL6		FLEXI03_D03	ADC1_IN8, ADC2_IN8	ACMP2_IN3
16	A2	GPIO_AD_B1_07	FLEXSPI_B_DATA0	LPI2C3_SCL	LPUART3_RXD	SPDIF_EXT_CLK	CSI_HSYNC	GPIO1_I023	USDHC2_DATA3	KPP_COL4		FLEXI03_D07	ADC1_IN12, ADC2_IN12	ACMP1_IN5
17	A3	GPIO_AD_B1_06	FLEXSPI_B_DATA1	LPI2C3_SDA	LPUART3_TXD	SPDIF_LOCK	CSI_VSYNC	GPIO1_I022	USDHC2_DATA2	KPP_ROW4		FLEXI03_D06	ADC1_IN11, ADC2_IN11	ACMP1_IN1, ACMP2_IN1, ACMP3_IN1, ACMP4_IN1
18	A4	GPIO_AD_B1_01	USB_OTG1_PWR	TMR3_TIMER1	LPUART2_RTS_B	LPI2C1_SDA	CCM_PMIC_READY	GPIO1_I017	USDHC1_VSELECT	KPP_COL7	ENET2_1588_EVENT0_IN	FLEXI03_D01	ADC1_IN6, ADC2_IN6	ACMP1_IN0, ACMP2_IN0, ACMP3_IN0, ACMP4_IN0
19	A5	GPIO_AD_B1_00	USB_OTG2_ID	TMR3_TIMER0	LPUART2_CTS_B	LPI2C1_SCL	WDOG1_B	GPIO1_I016	USDHC1_WP	KPP_ROW7	ENET2_1588_EVENT0_OUT	FLEXI03_D00	ADC1_IN5, ADC2_IN5	ACMP4_IN2
20	A6	GPIO_AD_B1_10	FLEXSPI_A_DATA3	WDOG1_B	LPUART8_TXD	SAI1_RX_SYNC	CSI_DATA07	GPIO1_I026	USDHC2_WP	KPP_ROW2	ENET2_1588_EVENT1_OUT	FLEXI03_D10	ADC1_IN15, ADC2_IN15	ACMP4_IN5
21	A7	GPIO_AD_B1_11	FLEXSPI_A_DATA2	EWM_OUT_B	LPUART8_RXD	SAI1_RX_BCLK	CSI_DATA06	GPIO1_I027	USDHC2_RESET_B	KPP_COL2	ENET2_1588_EVENT1_IN	FLEXI03_D11	ADC1_IN0, ADC2_IN0	ACMP1_IN6
22	A8	GPIO_AD_B1_08	FLEXSPI_A_SS1_B	FLEXPWM4_PWM0_A	FLEXCAN1_TX	CCM_PMIC_READY	CSI_DATA09	GPIO1_I024	USDHC2_CMD	KPP_ROW3		FLEXI03_D08	ADC1_IN13, ADC2_IN13	ACMP2_IN5
23	A9	GPIO_AD_B1_09	FLEXSPI_A_DQS	FLEXPWM4_PWM1_A	FLEXCAN1_RX	SAI1_MCLK	CSI_DATA08	GPIO1_I025	USDHC2_CLK	KPP_COL3		FLEXI03_D09	ADC1_IN14, ADC2_IN14	ACMP3_IN5
24	A10	GPIO_AD_B0_12	LPI2C4_SCL	CCM_PMIC_READY	LPUART1_TXD	WDOG2_B	FLEXPWM1_PWM2_X	GPIO1_I012	ENET_1588_EVENT1_OUT				ADC1_IN1	
25	A11	GPIO_AD_B0_13	LPI2C4_SDA	GPT1_CLK	LPUART1_RXD	EWM_OUT_B	FLEXPWM1_PWM3_X	GPIO1_I013	ENET_1588_EVENT1_IN	REF_CLK_24M			ADC1_IN2	ACMP1_IN2
26	A12	GPIO_AD_B1_14	FLEXSPI_A_SCLK		LPSP13_SOUT	SAI1_TX_BCLK	CSI_DATA03	GPIO1_I030	USDHC2_DATA6	KPP_ROW0	ENET2_1588_EVENT3_OUT	FLEXI03_D14	ADC2_IN3	ACMP3_OUT, ACMP4_IN6
27	A13	GPIO_AD_B1_15	FLEXSPI_A_SS0_B		LPSP13_SCK	SAI1_TX_SYNC	CSI_DATA02	GPIO1_I031	USDHC2_DATA7	KPP_COL0	ENET2_1588_EVENT3_IN	FLEXI03_D15	ADC2_IN4	ACMP4_OUT
28		GPIO_EMC_32	SEMC_DATA10	FLEXPWM3_PWM1_B	LPUART7_RXD	CCM_PMIC_READY	CSI_DATA21	GPIO3_I018			ENET2_TX_EN			
29		GPIO_EMC_31	SEMC_DATA09	FLEXPWM3_PWM1_A	LPUART7_TXD	LPSP11_PCS1	CSI_DATA22	GPIO4_I031			ENET2_TDATA1			
30		GPIO_EMC_37	SEMC_DATA15	XBAR_INOUT23	GPT1_COMPARE3	SAI3_MCLK	CSI_DATA16	GPIO3_I023	USDHC2_WP		ENET2_RX_EN	FLEXCAN3_RX		
31		GPIO_EMC_36	SEMC_DATA14	XBAR_INOUT22	GPT1_COMPARE2	SAI3_TX_DATA	CSI_DATA17	GPIO3_I022	USDHC1_WP		ENET2_RDATA1	FLEXCAN3_TX		
32		GPIO_B0_12	LCD_DATA08	XBAR_INOUT10	ARM_TRACE_CLK	SAI1_TX_DATA1, SAI1_RX_DATA3	FLEXI02_D12	GPIO2_I012	SRC_BT_CFG08		ENET2_TDATA0			
33		GPIO_EMC_07	SEMC_DATA07	FLEXPWM2_PWM0_B	SAI2_MCLK	XBAR_INOUT09	FLEXI01_D07	GPIO4_I07	USB_PHY1_TSTO_RX_FS_RXD					
34		GPIO_SD_B0_03	USDHC1_DATA1	FLEXPWM1_PWM1_B	LPUART8_RTS_B	XBAR_INOUT07	LPSP11_SIN	GPIO3_I015			ENET2_RDATA0			
35		GPIO_SD_B0_02	USDHC1_DATA0	FLEXPWM1_PWM1_A	LPUART8_CTS_B	XBAR_INOUT06	LPSP11_SOUT	GPIO3_I014			ENET2_RX_ER			
36		GPIO_SD_B0_01	USDHC1_CLK	FLEXPWM1_PWM0_B	LPI2C3_SDA	XBAR_INOUT05	LPSP11_PCS0	GPIO3_I013	FLEXSPI_B_SS1_B		ENET2_TX_CLK	ENET2_REF_CLK2		
37		GPIO_SD_B0_00	USDHC1_CMD	FLEXPWM1_PWM0_A	LPI2C3_SCL	XBAR_INOUT04	LPSP11_SCK	GPIO3_I012	FLEXSPI_A_SS1_B		ENET2_TX_EN			
38		GPIO_SD_B0_05	USDHC1_DATA3	FLEXPWM1_PWM2_B	LPUART8_RXD	XBAR_INOUT09	FLEXSPI_B_DQS	GPIO3_I017	CCM_CLK02		ENET2_RX_EN			
39		GPIO_SD_B0_04	USDHC1_DATA2	FLEXPWM1_PWM2_A	LPUART8_TXD	XBAR_INOUT08	FLEXSPI_B_SS0_B	GPIO3_I016	CCM_CLK01		ENET2_RDATA1			