

Table 86. 12 x 12 mm functional contact assignments (continued)

GPIO_AD_B0_00	M14	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[0]	Input	Keeper
GPIO_AD_B0_01	H10	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[1]	Input	Keeper
GPIO_AD_B0_02	M11	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[2]	Input	Keeper
GPIO_AD_B0_03	G11	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[3]	Input	Keeper
GPIO_AD_B0_04	F11	NVCC_GPIO	Digital GPIO	ALT0	SRC.BOOT.MODE[0]	Input	100 K PD
GPIO_AD_B0_05	G14	NVCC_GPIO	Digital GPIO	ALT0	SRC.BOOT.MODE[1]	Input	100 K PD
GPIO_AD_B0_06	E14	NVCC_GPIO	Digital GPIO	ALT0	JTAG.MUX.TMS	Input	47 K PU
GPIO_AD_B0_07	F12	NVCC_GPIO	Digital GPIO	ALT0	JTAG.MUX.TCK	Input	47 K PU
GPIO_AD_B0_08	F13	NVCC_GPIO	Digital GPIO	ALT0	JTAG.MUX.MOD	Input	100 K PU
GPIO_AD_B0_09	F14	NVCC_GPIO	Digital GPIO	ALT0	JTAG.MUX.TDI	Input	47 K PU
GPIO_AD_B0_10	G13	NVCC_GPIO	Digital GPIO	ALT0	JTAG.MUX.TDO	Input	Keeper
GPIO_AD_B0_11	G10	NVCC_GPIO	Digital GPIO	ALT0	JTAG.MUX.TRSTB	Input	47 K PU
GPIO_AD_B0_12	K14	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[12]	Input	Keeper
GPIO_AD_B0_13	L14	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[13]	Input	Keeper
GPIO_AD_B0_14	H14	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[14]	Input	Keeper
GPIO_AD_B0_15	L10	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[15]	Input	Keeper
GPIO_AD_B1_00	J11	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[16]	Input	Keeper
GPIO_AD_B1_01	K11	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[17]	Input	Keeper
GPIO_AD_B1_02	L11	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[18]	Input	Keeper
GPIO_AD_B1_03	M12	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[19]	Input	Keeper
GPIO_AD_B1_04	L12	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[20]	Input	Keeper

Table 86. 12 x 12 mm functional contact assignments (continued)

GPIO_AD_B1_05	K12	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[21]	Input	Keeper
GPIO_AD_B1_06	J12	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[22]	Input	Keeper
GPIO_AD_B1_07	K10	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[23]	Input	Keeper
GPIO_AD_B1_08	H13	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[24]	Input	Keeper
GPIO_AD_B1_09	M13	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[25]	Input	Keeper
GPIO_AD_B1_10	L13	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[26]	Input	Keeper
GPIO_AD_B1_11	J13	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[27]	Input	Keeper
GPIO_AD_B1_12	H12	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[28]	Input	Keeper
GPIO_AD_B1_13	H11	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[29]	Input	Keeper
GPIO_AD_B1_14	G12	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[30]	Input	Keeper
GPIO_AD_B1_15	J14	NVCC_GPIO	Digital GPIO	ALT5	GPIO1.IO[31]	Input	Keeper
GPIO_B0_00	D7	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[0]	Input	Keeper
GPIO_B0_01	E7	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[1]	Input	Keeper
GPIO_B0_02	E8	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[2]	Input	Keeper
GPIO_B0_03	D8	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[3]	Input	Keeper
GPIO_B0_04	C8	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[4]	Input	Keeper
GPIO_B0_05	B8	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[5]	Input	Keeper
GPIO_B0_06	A8	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[6]	Input	Keeper
GPIO_B0_07	A9	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[7]	Input	Keeper
GPIO_B0_08	B9	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[8]	Input	Keeper
GPIO_B0_09	C9	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[9]	Input	Keeper

Table 86. 12 x 12 mm functional contact assignments (continued)

GPIO_B0_10	D9	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[10]	Input	Keeper
GPIO_B0_11	A10	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[11]	Input	Keeper
GPIO_B0_12	C10	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[12]	Input	Keeper
GPIO_B0_13	D10	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[13]	Input	Keeper
GPIO_B0_14	E10	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[14]	Input	Keeper
GPIO_B0_15	E11	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[15]	Input	Keeper
GPIO_B1_00	A11	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[16]	Input	Keeper
GPIO_B1_01	B11	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[17]	Input	Keeper
GPIO_B1_02	C11	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[18]	Input	Keeper
GPIO_B1_03	D11	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[19]	Input	Keeper
GPIO_B1_04	E12	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[20]	Input	Keeper
GPIO_B1_05	D12	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[21]	Input	Keeper
GPIO_B1_06	C12	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[22]	Input	Keeper
GPIO_B1_07	B12	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[23]	Input	Keeper
GPIO_B1_08	A12	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[24]	Input	Keeper
GPIO_B1_09	A13	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[25]	Input	Keeper
GPIO_B1_10	B13	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[26]	Input	Keeper
GPIO_B1_11	C13	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[27]	Input	Keeper
GPIO_B1_12	D13	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[28]	Input	Keeper
GPIO_B1_13	D14	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[29]	Input	Keeper
GPIO_B1_14	C14	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[30]	Input	Keeper

Table 86. 12 x 12 mm functional contact assignments (continued)

GPIO_B1_15	B14	NVCC_GPIO	Digital GPIO	ALT5	GPIO2.IO[31]	Input	Keeper
GPIO_EMC_00	E3	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[0]	Input	Keeper
GPIO_EMC_01	F3	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[1]	Input	Keeper
GPIO_EMC_02	F4	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[2]	Input	Keeper
GPIO_EMC_03	G4	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[3]	Input	Keeper
GPIO_EMC_04	F2	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[4]	Input	Keeper
GPIO_EMC_05	G5	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[5]	Input	Keeper
GPIO_EMC_06	H5	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[6]	Input	Keeper
GPIO_EMC_07	H4	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[7]	Input	Keeper
GPIO_EMC_08	H3	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[8]	Input	Keeper
GPIO_EMC_09	C2	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[9]	Input	Keeper
GPIO_EMC_10	G1	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[10]	Input	Keeper
GPIO_EMC_11	G3	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[11]	Input	Keeper
GPIO_EMC_12	H1	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[12]	Input	Keeper
GPIO_EMC_13	A6	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[13]	Input	Keeper
GPIO_EMC_14	B6	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[14]	Input	Keeper
GPIO_EMC_15	B1	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[15]	Input	Keeper
GPIO_EMC_16	A5	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[16]	Input	Keeper
GPIO_EMC_17	A4	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[17]	Input	Keeper
GPIO_EMC_18	B2	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[18]	Input	Keeper
GPIO_EMC_19	B4	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[19]	Input	Keeper

Table 86. 12 x 12 mm functional contact assignments (continued)

GPIO_EMC_20	A3	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[20]	Input	Keeper
GPIO_EMC_21	C1	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[21]	Input	Keeper
GPIO_EMC_22	F1	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[22]	Input	Keeper
GPIO_EMC_23	G2	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[23]	Input	Keeper
GPIO_EMC_24	D3	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[24]	Input	Keeper
GPIO_EMC_25	D2	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[25]	Input	Keeper
GPIO_EMC_26	B3	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[26]	Input	Keeper
GPIO_EMC_27	A2	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[27]	Input	100 K PD
GPIO_EMC_28	D1	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[28]	Input	Keeper
GPIO_EMC_29	E1	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[29]	Input	Keeper
GPIO_EMC_30	C6	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[30]	Input	Keeper
GPIO_EMC_31	C5	NVCC_EMC	Digital GPIO	ALT5	GPIO4.IO[31]	Input	Keeper
GPIO_EMC_32	D5	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[18]	Input	Keeper
GPIO_EMC_33	C4	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[19]	Input	Keeper
GPIO_EMC_34	D4	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[20]	Input	Keeper
GPIO_EMC_35	E5	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[21]	Input	Keeper
GPIO_EMC_36	C3	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[22]	Input	Keeper
GPIO_EMC_37	E4	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[23]	Input	Keeper
GPIO_EMC_38	D6	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[24]	Input	Keeper
GPIO_EMC_39	B7	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[25]	Input	Keeper
GPIO_EMC_40	A7	NVCC_EMC	Digital GPIO	ALT5	GPIO3.IO[26]	Input	Keeper